

BIG MAC DATA SHEET

The Big Mac is a high performance Macintosh which can be used as both a Macintosh and as a work station. It is implemented using three 500 gate bipolar gate arrays and one 1200 gate cmos gate array.

- Display:** 1024 x 800 B/W display, 60 Hz refresh, 70Mhz dot rate
- CPU:** 68020 with option for 12M, 16M, and 18M clock rate
- FPU:** 68881 as floating point coprocessor
- MMU:** Custom MMU with 4Gb address space, 64 entry TLB with 4Kb pages which offers a working set size of 256Kb. Virtual address space is configurable from 16M to 4Gb. The MMU supports write and execute protection and dirty bits for each page. *Signature a G.E.*
- Memory:** 2 Mb of DRAM on main logic board using 256Kx1
Separate video memory using 64Kx4
256Kb of ROM using two 1M parts
RAM is limited to 10Mb max
- DMA:** 68450 (use 68440 if no 2nd SCC) with support to access the full 32 bit address space.
- I/O:** Two SCC chips (ie. four serial ports)
SCSI port for internal 20Mb hard disk and external devices
Double sided SONY, IWM chip interface, option for 2nd SONY
Macintosh compatible sound with SONY sound chip.
Front Desk Bus for keyboard, mouse, etc.
Real Time Clock with battery backup
Soft power on/off.
- Slots:** Four I/O Slots with access to 32 bit address and data bus
Two Memory Expansion Slots

BIG MAC LOGIC BLOCK DIAGRAM

Rich Page
6/2/85

