

### 3.5.3 Head Position Initialization

At power on, the head shall be automatically accessed to track 0.

### 3.6 Interface Connector and Pin Assignment

The interface connector shall be a 20 pin connector, 3M J3428-5202 or equivalent. The pinouts are as follows:

<u>Pin number</u>	<u>Signal Name</u>	<u>Pin Number</u>	<u>Signal Name</u>
1	GND	2	CA0
3	GND	4	CA1
5	GND	6	CA2
7	GND	8	LSTRB
9	N/C	10	/WRTGATE
11	+5V	12	SEL
13	+12V	14	/ENBL
15	+12V	16	RD
17	+12V	18	WRTDATA
19	+12V	20	/PWM

### 4.0 Labelling

The drive shall have two labels attached when it is shipped to Apple.

#### 4.1 Label Position

The serial number label shall be attached to the right side, and the date label to the left side of the chassis as shown in Figure 4.1.

#### 4.2 Label Contents

The shape and contents of the serial number label shall be as shown in Figure 4.2. The date label shape and size may be picked by the drive manufacturer, but must include the month and year of manufacture and be clearly legible.



apple computer inc.

SIZE  
A

DRAWING NUMBER  
699-0285-A

SCALE:

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