

2.10 Time Margin

2.10.1 Definition of time margin

Time margin is measured using the Apple jitter generating fixture. This circuit jitters the read pulse coming from the drive under test randomly. The time margin is defined as the largest value of time that the read pulse can be jittered while still allowing the controller to read with fewer than one error in ten million bits read. The schematic of the jitter generator, Apple part number 890-2002, is shown in Appendix A.

The data read is comprised of a random pattern of flux changes including all legal combinations of 2, 4, & 6 usec periods between flux changes.

Track format and Sector format is defined in Appendix B.

2.10.2 Self read/write time margin

The self read/write time margin shall be: >300 nS

2.10.3 Off-track Time Margin

The time margin using a reference disk on which random data is written +0.035 mm and -0.035 mm off track shall be >300 ns.



apple computer inc.

SIZE
A

DRAWING NUMBER
699-0285-A

SCALE:

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